

[A LINE INVERSION DRIVE DEVICE FOR THIN FILM TRANSISTOR LIQUID CRYSTAL DISPLAY]

Abstract

The present invention also provides a line inversion drive circuit for a thin film transistor liquid crystal display. The line inversion drive circuit comprises a clock controller and a data line driver. The clock controller includes a data inversion circuit for receiving a data signal and a clock control device; the data inversion circuit is coupled to the clock control device; the data inversion circuit responsive to an inversion control signal determines whether to invert the data signal and outputs a display signal. The data line driver, coupled to the data inversion device, is for receiving a group of reference voltages; the data line driver responsive the group of reference voltages and the display signal drives a plurality of data lines of the thin film transistor liquid crystal display. The present invention uses a single group of reference voltages only for both non-inverse and inverse data signals.